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S/N 10/593674

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Tanimura

Examiner:

Kyung S. Lee

Serial No.:

10/593674

Group Art Unit:

2833

Filed:

September 21, 2006

Docket No.:

10921.0427USWO

Title:

CHIP RESISTOR AND MANUFACTURING METHOD THEREOF

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on September 3,

## RESPONSE TO ELECTION OF SPECIES REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir.

Applicants elect Group I (claims 1-5, 13 and 14) drawn to the embodiment shown in Figure 2 in response to the Election of Species Requirement of August 3, 2009.

An early and favorable action on the merits is requested.

Please charge any additional fees or credit any overpayment to Deposit Account

No. 50-3478.

52835

PATENT TRADEMARK OFFICE

Respectfully submitted,

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Date: September 3, 2009

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